AMENDMENTS TO THE CLAIMS

Please amend Claim 1 as follows.

1. (Currently amended) A pixel structure of a liquid crystal display, wherein said liquid crystal display comprises a first substrate whereon said pixel structure is disposed, a second substrate whereon a conductor electrode is disposed and a liquid crystal layer sandwiched between said first substrate and said second substrate, said pixel structure comprising:

a plurality of scan lines located on said first substrate and arranged in a first direction and parallel to each other;

a plurality of common electrode lines located on said first substrate and arranged in the first direction and parallel to each other, wherein said plurality of scan lines and said plurality of common electrode lines are alternatingly located in the first direction;

a plurality of metal lines located on said first substrate and expanded from said plurality of common electrode lines;

a first insulating layer located over said plurality of scan lines, said plurality of common electrode lines and said plurality of metal lines;

a plurality of video data lines located on said first insulating layer and arranged in parallel to each other and arranged in a second direction to cross said plurality of common electrode lines and metal lines, wherein any adjacent scan lines and any adjacent video data lines define a pixel region, each pixel region comprising one of said plurality of common electrode lines and one of said plurality of metal lines expanding therefrom;

a second insulating layer located over said plurality of video data lines;

a plurality of pixel electrodes located over said second insulating layer, each pixel electrode located at a corresponding pixel region, wherein each pixel electrode is divided into a plurality of sub pixel electrodes, a gap is located between each of adjacent sub pixel electrodes, and at least a portion of each metal line is located between adjacent sub pixel electrodes; and

a plurality of switch transistors respectively located at said video data lines crossing said scan lines positions, wherein gate electrodes of said switch transistors are coupled to said scan lines and said video data lines are coupled to said pixel electrodes through said switch transistors.

2. (Original) The pixel structure of claim 1, wherein said first direction is perpendicular to said second direction.

3. (Original) The pixel structure of claim 1, wherein said pixel electrode is formed with an ITO or IZO material.

4. (Original) The pixel structure of claim 1, wherein said metal lines partially overlap with corresponding pixel electrode to form a capacitor structure.

5. (Original) The pixel structure of claim 1, wherein said metal lines are arranged around said pixel regions.

6. (Original) The pixel structure of claim 1, wherein said metal lines are arranged inside of said pixel regions.

7-19.(Cancelled)

SUMMARY OF INTERVIEW

Exhibits and/or Demonstrations

None

Identification of Claims Discussed

Claim 1

Identification of Prior Art Discussed

Tsubo (U.S. Patent No. 6,831,295)

Proposed Amendments

None

Principal Arguments and Other Matters

Applicant argued that Tsubo does not teach or suggest "each pixel electrode is *divided into a plurality of sub pixel electrodes*, and at least a portion of each metal line is *located between adjacent sub pixel electrodes*" recited in Claim 1 since Tsubo teaches only one pixel electrode (13) in each pixel region as shown in Figures 11 and 13.

The Examiner basically agreed with Applicant regarding the distinction between the claimed invention and Tsubo. However, after discussing with his supervisor, the Examiner indicated that Claim 1 would be allowable over the prior art of record if amended to add the feature (or similar thereto) "adjacent sub pixel electrodes are separated by a gap."